

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: JOSEPH HUANG ET AL.
Filed: JANUARY 15, 2004
For: APPARATUS AND METHODS FOR PROVIDING REDUNDANCY
IN INTEGRATED CIRCUITS

Serial No.: 10/757,928
Group Art Unit: 2827
Examiner: WEINBERG, MICHAEL J.
Atty Dkt: ALTR:023

I hereby certify that this correspondence is being transmitted electronically to the USPTO on the date shown below.

Date: July 20, 2006

Name:



ATTN: OFFICIAL DRAFTSMAN
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

SUBMISSION OF FORMAL DRAWINGS

Applicant hereby submits seven (7) sheets of formal drawings (not replacement sheets)
for the above-referenced application and request that these drawings be accepted for filing.

Respectfully submitted,



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